DC/DC Converter SK78xx-1000R3(L) Series



Wide input voltage , non-isolated & regulated single output





FEATURES

- High efficiency up to 96%
- No-load input current as low as 0.1mA
- Operating temperature range: -40℃ to +85℃
- Support the negative output
- Output short circuit protection
- Pin-out compatible with LM78XX linear regulators
- Meets UL60950, EN60950 standards (Pending)

SK78xx-1000R3(L) series are high efficiency switching regulators and ideal substitutes of SLM78xx series three-terminal linear regulators. The product is featured with high efficiency, low loss and no heat sink requirement. They are widely used in industrial control, instrumentation, and electric power applications.

Certification	Part Number	Input Voltage (VDC) Output			Efficiency (%/Typ.)	Max.
		Nominal (Range)	Output Voltage (VDC)	Max. Output Current (mA)	(Min. Vin)/ (Max. Vin) @Full Load	Capacitive Load(µF)
	SK7803-1000R3(L)	24 (6-36)	3.3	1000	90/81	680
	SK7805-1000R3(L)	24 (8-36)	5.0 1000		93/86	680
		12 (8-27)	-5.0	-500	86/82	330
UL/CE	SK7809-1000R3(L)	24 (13-36)	9	1000	95/90	680
(Pending)	SK7812-1000R3(L)	24 (16-36)	12	1000	96/93	680
		12 (8-20)	-12	-300	89/88	330
	SK7815-1000R3(L)	24 (20-36)	15	1000	96/94	680
		12 (8-18)	-15	-300	89/89	330

Input Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
No-load Input Current	Positive output		0.1	1	mA	
Reverse Polarity Input	leverse Polarity Input Forbidden					
Input Filter	Capacitor filter					

Output Specifications							
Item	Operating Conditions	Min.	Тур.	Max.	Unit		
Output Voltage Accuracy	Full load, input voltage range	SK7803-1000R3(L)		±2	±4	%	
Output Voltage Accuracy	raii load, iripui voilage tarige	Others		±2	±3		
Line Regulation	Full load, input voltage range			±0.2	±0.4		
Load Regulation	Nominal input, 10% - 100% load			±0.4	±0.6		
Ripple & Noise*	20MHz bandwidth, nominal inpu	20MHz bandwidth, nominal input, 20% -100% load			75	mVp-p	
Temperature Drift Coefficient	Operating temperature -40°C ~ -	Operating temperature -40° ~ +85° €			±0.03	%/℃	
Transient response deviation	Nominal input,			50	300	mV	
Transient recovery time	25%-50%-25% 50%-75%-50% loa		0.1	1	ms		
Output short circuit protection	Nominal input	Continuous, self-recovery					
Note: *1. Ripple and noise tested with "parallel cable" method, please refer to <i>DC-DC Converter Application Notes</i> for specific operation methods; *2. With the load lower than 20%, the maximum ripple and noise of 3.3V/5V output products will be 100mVp-p, 9V/12V/15V output products will be 2%Vo.							

Schmid Multitech GmbH - 1 -

General Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Operating Temperature	Derating if the temperate	-40		85		
Storage Temperature		-55		125	\mathbb{C}	
Pin Welding Resistance Temperature	Welding time: 10s (Max.)			260		
Storage Humidity	Non-condensing	5		95	%RH	
Outhor Francisco	Full land a sale of board	SK7803/05-1000R3(L)	420	520	620	KHz
Switching Frequency	Full load, nominal input	Others	580	680	780	
MTBF	MIL-HDBK-217F@25℃		2000	-		K hours

Physical Specifications					
Casing Material		Black flame-retardant and heat-resistant plastic (UL94 V-0)			
Packago Dimonsions	SK78xx-1000R3	11.50*9.00*17.50 mm			
Package Dimensions	SK78xx-1000R3L	17.50*11.50*9.00 mm			
Weight		3.8g (Typ.)			
Cooling Method		Free air convection			

EMC Specific	ations			
EMI	CE	CISPR22/EN55022	CLASS B (see Fig. 4-2) for recommended circuit)	
EIVII	RE	CISPR22/EN55022	CLASS B (see Fig. 4-2) for recommended circuit)	
	ESD	IEC/EN 61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN 61000-4-3	10V/m	perf. Criteria A
EMS	EFT	IEC/EN 61000-4-4	±1KV (see Fig. 4-① for recommended circuit)	perf. Criteria B
	Surge	IEC/EN 61000-4-5 circuit)	line to line ±1KV(see Fig. 4-① for recommended	perf. Criteria B
	CS	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A

Product Characteristic Curve

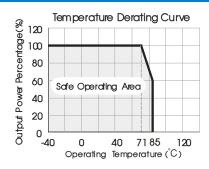
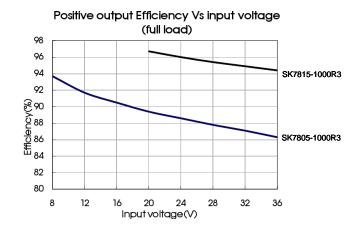
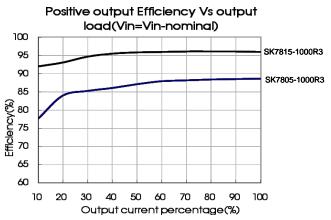
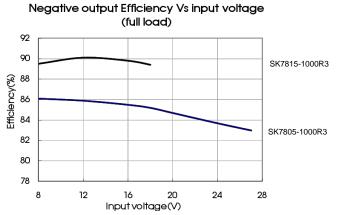
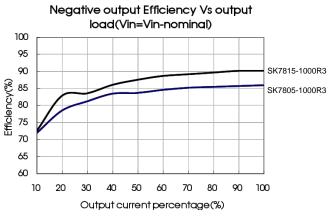


Fig. 1



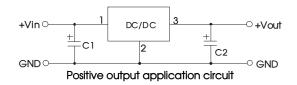






Design Reference

1. Typical application circuit



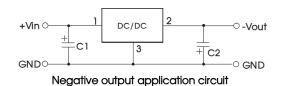
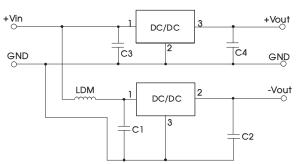


Fig. 2 Typical application circuit



Sheet 1							
Part No.	C1/C3	C2/C4					
Pair No.	(ceramic capacitor)	(ceramic capacitor)					
SK7803-1000R3(L)		22µF/10V					
SK7805-1000R3(L)	10μF/50V	22µF/10V					
SK7809-1000R3(L)		22µF/16V					
SK7812-1000R3(L)		22µF/25V					
SK7815-1000R3(L)		22µF/25V					

Fig. 3 Positive and Negative output parallelling application circuit

Note:

- 1. C1 and C2 (C3 and C4) are required and should be connected close to the pin terminal of the module.
- 2. The capacitance of C1 and C2 (C3 and C4) refer to Sheet 1.
- 3. To reduce the output ripple furtherly. C2 and C4 can be increased properly if required, and tantalum or low ESR electrolytic capacitors may also suffice.
- 4. When the products used as the circuit like figure 3, an inductor named as LDM up to 10µH is recommended in the circuit to reduce the mutual interference.
- 5. Cannot be used in parallel for output and hot swap.

2. EMC solution-recommended circuit

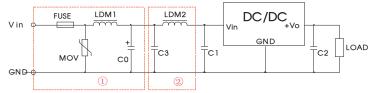
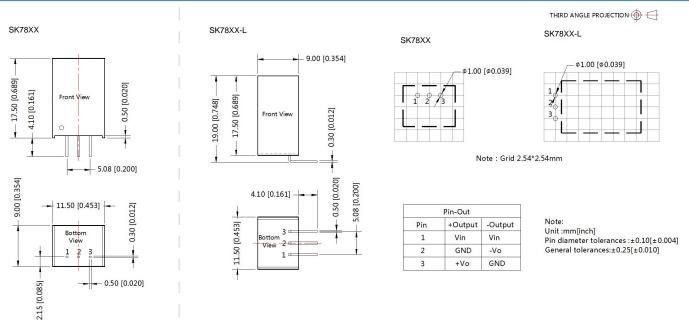


Fig. 4 EMC recommended circuit

FUSE	MOV	LDM1	C0	C1/C2	C3	LDM2
Selected based on the actual	S20K30	82µH	680µF /50V	Refer to Sheet 1	4.7µF /50V	12µH
input current from the customer	32000	οΖμιτ	000µi /300	Kelei io sileei i	4.7µ1 /30V	ιΖμιι

Note: Part ① in the Fig. 4 is for EMS test, part ② is for EMI filtering; parts ① and ② can be added based on actual requirement.

Dimensions and Recommended Layout



Notes:

- 1. The maximum capacitive load offered were tested at nominal input voltage and full load;
- 2. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75% with nominal input voltage and rated output load;
- 3. All index testing methods in this datasheet are based on our Company's corporate standards;
- 4. We can provide product customization service, please contact our technicians directly for specific information;
- 5. Specifications are subject to change without prior notice.